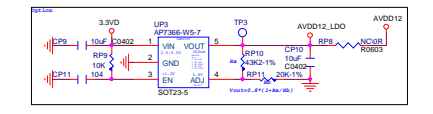
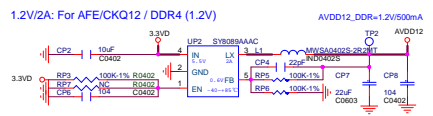
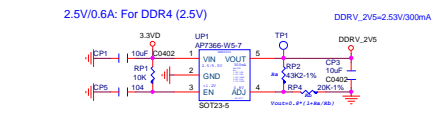
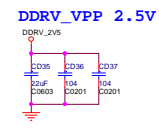
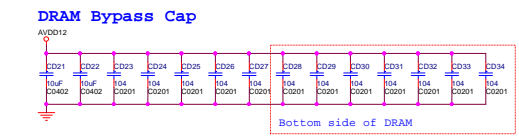
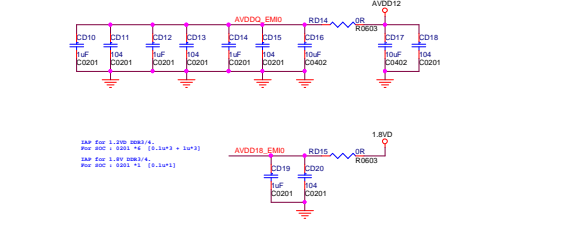
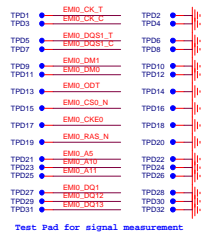
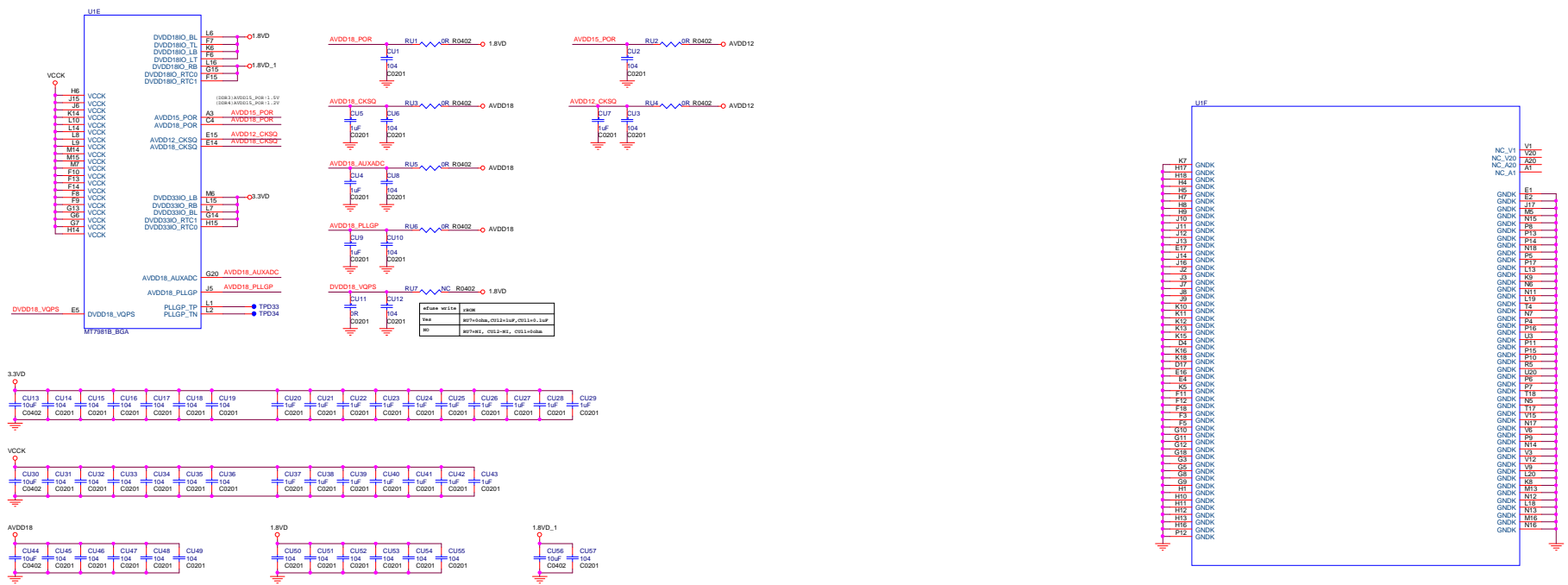
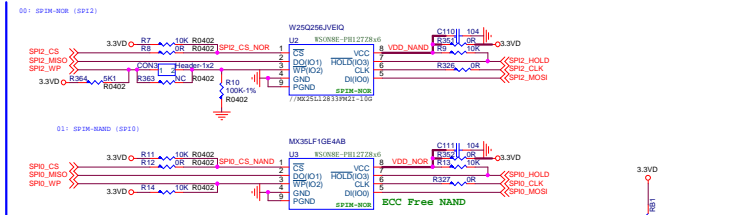
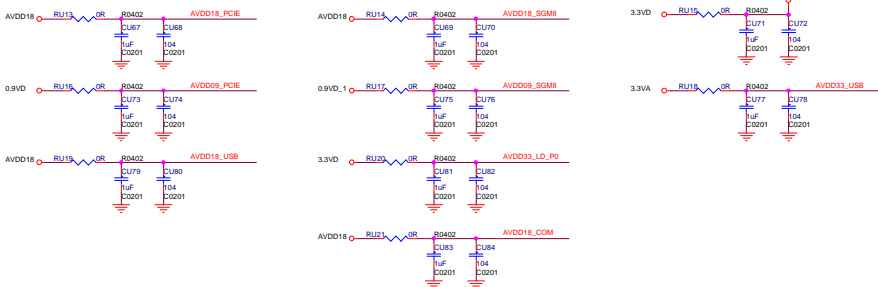
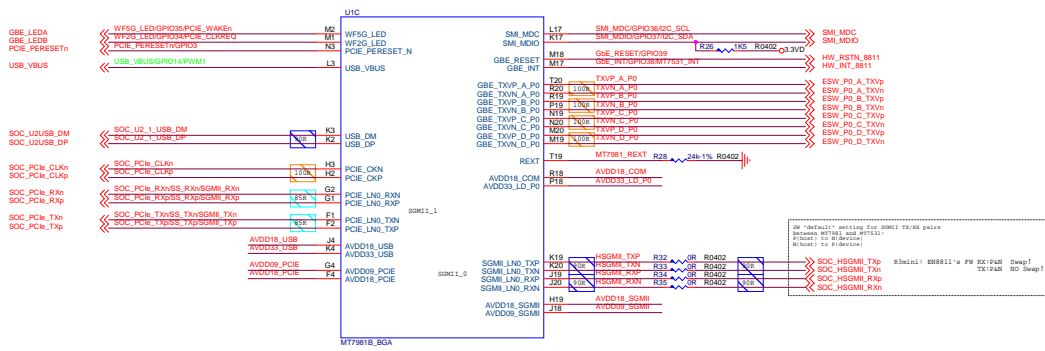
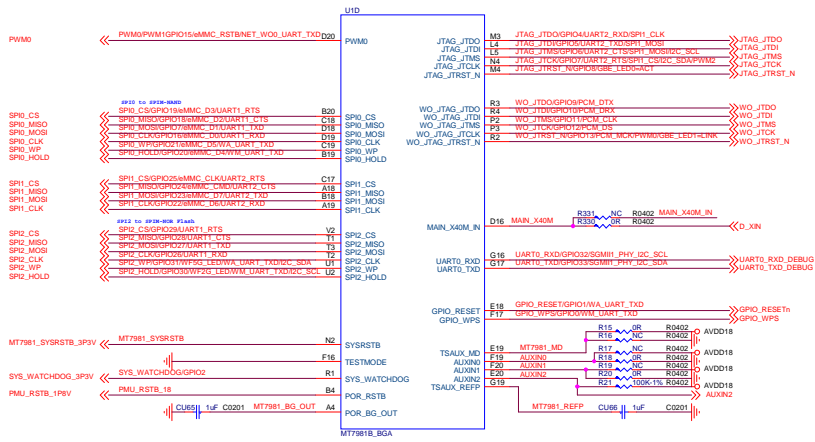


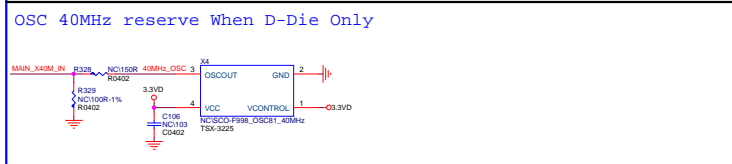
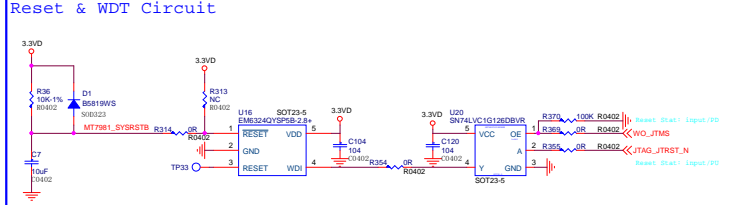
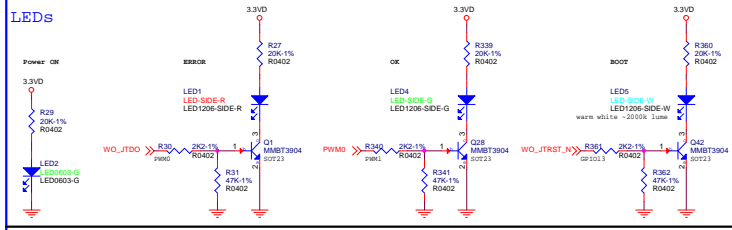
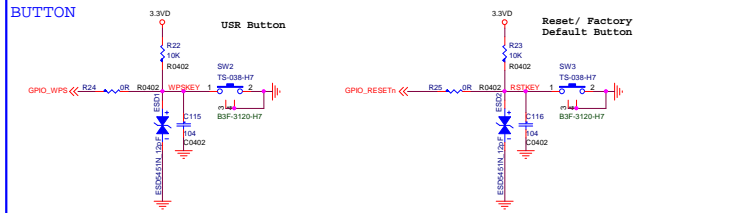
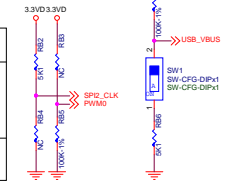
closed to UD1



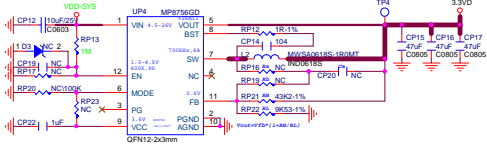




Boot Strapping Pin Name	Description	Boot Sequence
{PFM0, USB_VBUS}	Boot mode	00 : SPI-NOR 01 : SPI-NAND==>SD 10 : eMMC 11 : SNAND (SFNI) ==>SD
SPI2_CLK	40MHz Xtal select	1 : 40MHz

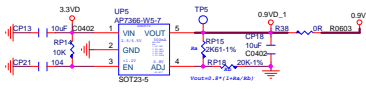


3.3V/6A



300mil power plane

0.9V/300mA: For SGMI/USB3/PCIE



Power on sequence

3.3V

0.85V

1.8V

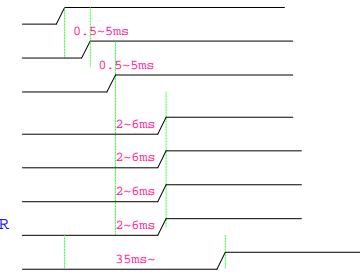
LDO 0.9V

LDO 1.2V

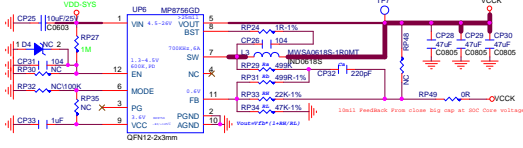
LDO 1.8V

LDO 2.5V DDR

SYSRST_B

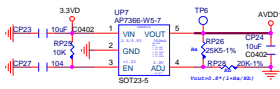


0.85V/6A

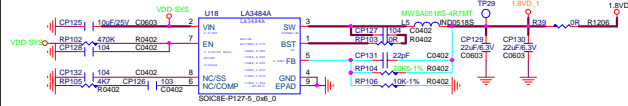


300mil power plane

1.8V/300mA: For CKSQ18/SGMI/USB/PCIE

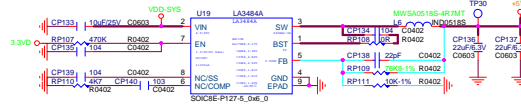


1.8V/3A

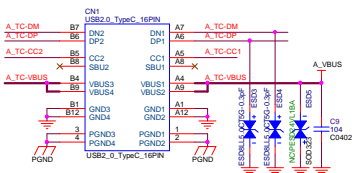


150mil power plane

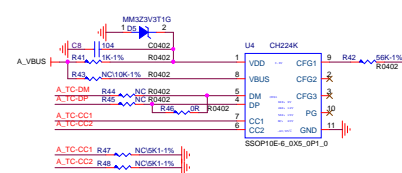
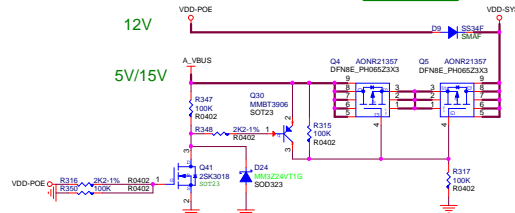
5V/3A :For USB device



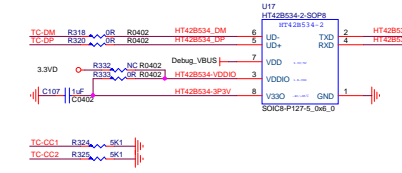
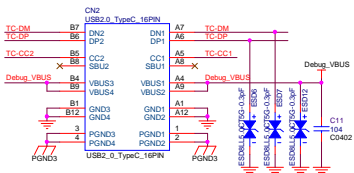
TypeC PD Power



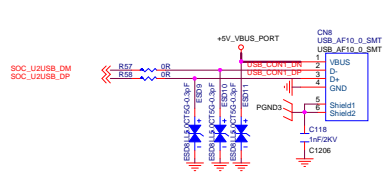
300mil power plane



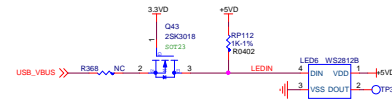
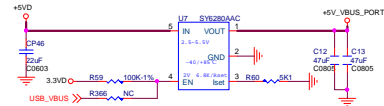
TypeC Debug

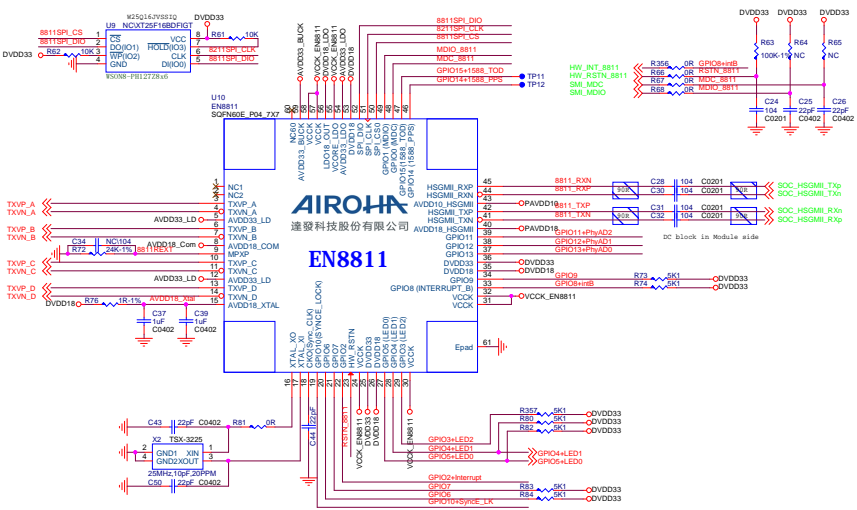


USB2.0

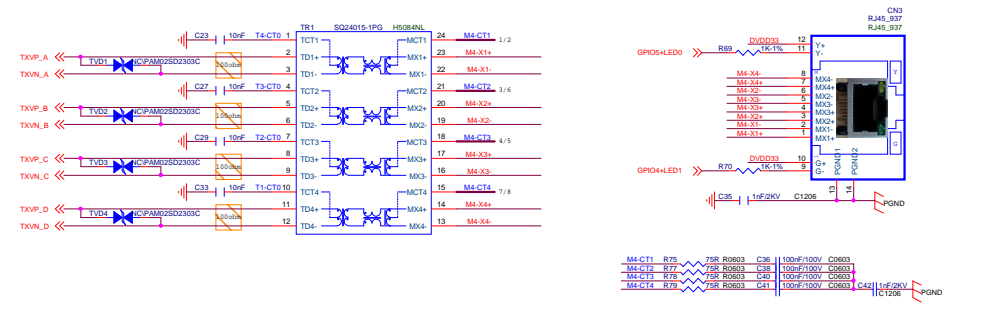


1330mA



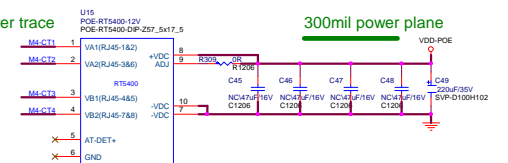


2.5G RJ45

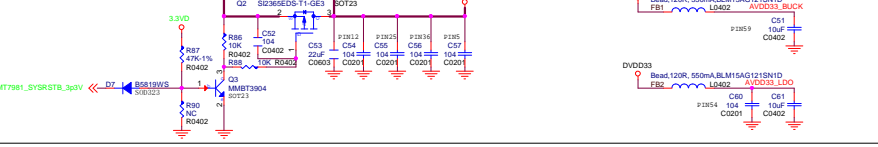


15mil power trace

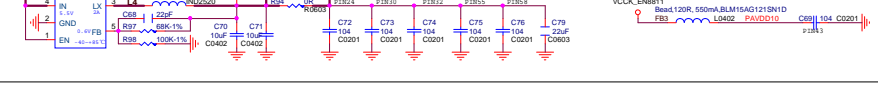
300mil power plane



3.3V 100mA VDDIO



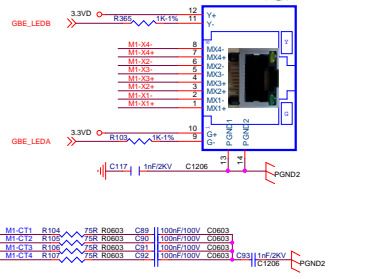
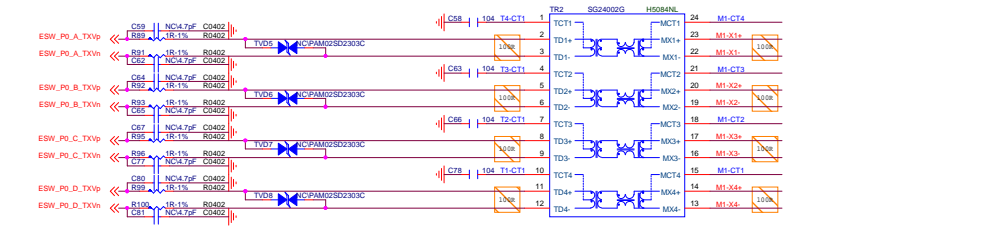
1.0V EN1V



1.8V EN1V8

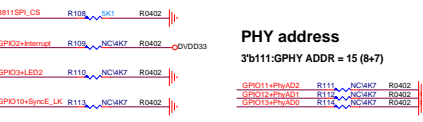


GbE RJ45



EN8811 Hardware Trap

Pin Name	Description	Default
SPI_CS(IPU)	ext flash code *1'b0: download code mode *1'b1: external flash code	1'b0
GPIO2(IPD)	m32 eflag_en *1'b0: disable *1'b1: enable	1'b0
GPIO3(IPU)	Co_clock_output_freq *1'b0: CKO pin output is 25MHz *1'b1: CKO pin output is 50MHz	1'b1
GPIO10(IPU)	xtal_mode *1'b0: Co-clock input mode *1'b1: XTAL mode	1'b1

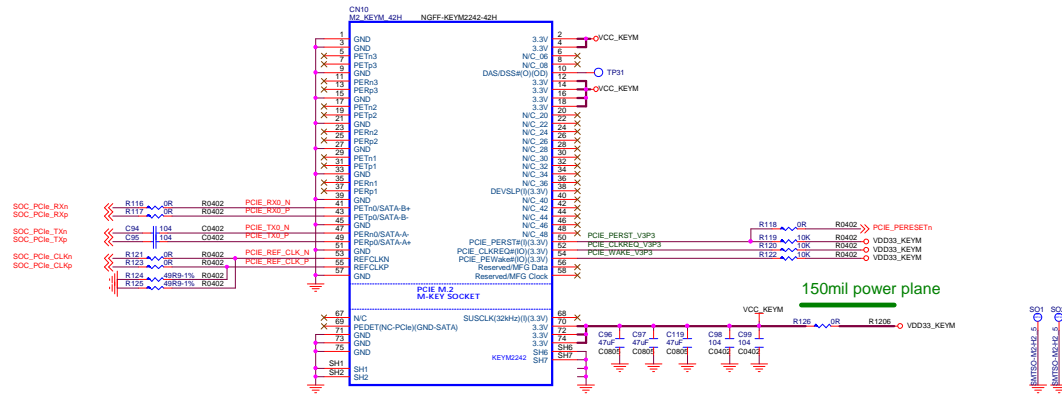
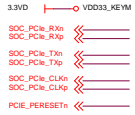


PHY address
3b11:GPHY ADDR = 15 (8+7)

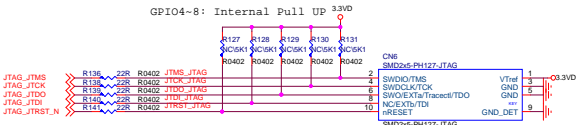


GUANGDONG BIPAI KEJI CPALTD			
	Design Name	BPL_OpenWRT_ONE_MT7981B	
	Size	Page Name	
	A2	06_Airoha_EN8811_2.5GPHY	
	Rev	Rev V1.0	
Date:		Thursday, March 06, 2020	Page 6 of 9

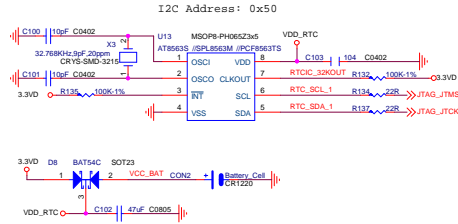
M-KEY



JTAG



RTC



mikroBUS

EEPROM(reserve for SN/MAC address/Calibration data)

